

IN THE CLAIMS:

Please cancel claim 7 without prejudice to or disclaimer of the subject matter recited therein.

Please amend claims 1 and 4 as follows:

LISTING OF CURRENT CLAIMS

1. (Currently Amended) A set of Universal Storage Interface Bus device, comprising:

- an IDE storage device, providing a first interface;
- a ribbon cable, providing cable having a first connector coupling with the IDE storage device and providing a second connector;
- an USIB connector conversion board, providing board having a third connector connector; and a fourth connector, the third connector coupling with connected to said second connector, converts the IDE signal and connector the USIB connector conversion board converts an IDE signal of the IDE data storage device into a USIB signal coming out of transmitted from the fourth connector, the fourth connector is a standard MC-36 connector; and
- an USIB interface conversion cable, providing a fifth connector, an USIB interface conversion board and a sixth connector, a cable being between and connecting with the USIB interface conversion board and the sixth connector, the fifth connector coupling with said fourth connector, and the sixth connector coupling with an input/out input/output port of a personal computer;
whereby, the data wherein the IDE storage device can be is connected to the input/output port with any type of interface by way of replacing with the appropriate by selecting a USIB interface conversion cable only.

2. (Original) The Universal Storage Interface Bus device according to claim 1, wherein the USIB interface conversion board further comprises an ASIC to convert a signal of the first interface into a signal of a second interface.

3. (Original) The Universal Storage Interface Bus device according to claim 1, wherein the first interface is IDE interface.

4. (Currently Amended) The Universal Storage Interface Bus device according to claim 2, wherein the second interface is a U**S** USB interface.

5. (Original) The Universal Storage Interface Bus device according to claim 2, wherein the second interface is a PCMCIA interface.

6. (Original) The Universal Storage Interface Bus device according to claim 2, wherein the second interface is a Firewire interface.

7. (Canceled)

8. (Original) The Universal Storage Interface Bus device according to claim 1, wherein the first, the second, and the third connectors are a 50-pin connector respectively.

9. (Original) The Universal Storage Interface Bus device according to claim 1, wherein the fourth and fifth connectors are a MC-36 connector respectively.

10. (Original) The Universal Storage Interface Bus device according to claim 9, wherein the respective MC-36 connector has 36 pins be defined as follows respectively: a 1st pin is RESRET, a 2nd pin is D7, a 3rd pin is D6, a 4th pin is D5, a 5th in is D4, a 6th pin is D3, a 7th pin is D2, a 8th pin is D1, a 9th pin is D0, a 10th pin is +5V, Device, 11th pin is 10W, 12th pin is IOR, 1 13th pin is ground, a 14th pin is DMAACK, a 15th pin is IRQ, a 16th pin is A1, a 17th pin is A0, a 18th pin is CS0, a 19th pin is GND, a 20th pin is D8, a 21st pin is D9, a 22nd pin is D10, a 23rd pin is D11, a 24th in is D12, a 25th in is D13, a 26th pin is D14, a 27th pin is D15, a 28th pin is Ground, a 29th in is Ground, a 30th pin is +12V, a 31st pin is *IORDY, a 32nd pin is IORDY/IOCS16, a 33rd pin is +5V, HOST, a 34th pin is A2, a 35th is a CS1, and a 36th pin is DMAREQ.